

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7	347/18.ccls. and resistors! with conduct\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:30
L2	1	347/18.ccls. and resistors! and conduct\$4 and circuit near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:31
L3	2	347/18.ccls. and resistors! and circuit near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:34
L4	4	347/18.ccls. and resistors and circuit near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:35
L5	21	347/17.ccls. and resistors and circuit near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:35
L6	12	347/17.ccls. and resistors! and circuit near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:35
L7	12	347/17.ccls. and resistors! and circuit near board and temperature	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:35
L8	1	347/17.ccls. and resistors! and circuit near board and package	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:36
L9	360	resistors! same circuit near board same package and temperature	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:36
L10	43	resistors! same circuit near board same package same temperature	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:38
L11	15	resistors! same circuit near board same package same temperature and temperature near control\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:43
L12	1	IC near package and resistor with circuit near board with package and temperature near control\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:44